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PATENT
Attorney Docket No. 944-003.108

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of: **J. Melava**

Serial No. **09/965,657**

Group Art Unit **2661**

Filed **September 26, 2001**

Conf. No. **3087**

Title: **FRACTIONAL MULTI-MODULUS PRESCALER**

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Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Technology Center 2600

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits herewith additional references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 CFR §1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 CFR §1.56(b), it is not intended to constitute an admission that any document referred to herein is "prior art" for this invention unless specifically designated as such.

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450,
Alexandria VA 22313-1450

Janet Gaffney 2/26/04
Janet Gaffney Date

In accordance with 37 CFR §1.97(g), the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that no other material information as defined under 37 CFR §1.56(a) exists.

This Supplemental Information Disclosure Statement is being submitted in response to a European Search Report dated February 2, 2004, which was issued in a corresponding counterpart European patent application. A copy of the European Search Report is enclosed. Applicant believes no fee is required, but in the event one is necessary, the Commissioner is hereby authorized to charge Deposit Account No. 23-0442.

Enclosed is a Form PTO-1449 listing the cited references with relevant copies thereof. Copies of U.S. patents are no longer required pursuant to the Official Gazette Notice dated August 5, 2003.

Respectfully submitted,

Dated: February 26, 2004

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FORM PTO-1449
INFORMATION DISCLOSURE STATEMENT

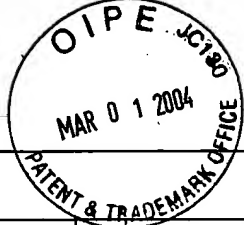
 ATTY DOCKET NO.
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 SERIAL NO.
 09/965,657

APPLICANT: Jari Melava

 FILING DATE:
 September 26, 2001

ART UNIT: 2661


UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL	DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,189,685	02/23/1993	Jaffard et al.			
	5,970,110	10/19/1999	Li			
	6,157,694	12/05/2000	Larsson			
	6,281,759	08/28/2001	Coffey			

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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
WO 01/45263 A1	06/21/2001	International			No

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	CRANINCKX J ET AL: "A 1.75-GHZ/3-V DUAL-MODULUS DIVIDE-BY-128/129 PRESCALER IN 0.7-MUM CMOS", IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. NEW YORK, US, VOL. 31, NO. 7, JULY 1, 1996, PAGES 890-897, XP000632373

Examiner (To be assigned)

Date: